What is claimed is:

1. A power supply control method in a system in which a power supply control device is provided for each of a plurality of information processing devices connected to a network, comprising:

an arbitrary information processing device of the plurality of information processing devices issuing, according to a predetermined power-up/down schedule of said arbitrary information processing device and other information processing devices, a power-up instruction to each power supply control device of the other information processing devices upon each activation;

instructing each of the information processing devices to perform a power-down process, notifying the information processing devices of a next power-up date and time, and having each power supply control device enter a next power-up date and time each time a power-down date and time comes; and

each power supply control device of said other information processing devices performing a power-up process upon receipt of the power-up instruction or when the entered power-up date and time comes.

2. A power supply control method in a system in which a power supply control device is provided for each of a plurality of information processing devices connected to a network, comprising:

an arbitrary information processing device of the plurality of information processing devices issuing, according to a predetermined power-up/down schedule of said arbitrary information processing device and other information processing devices, a power-up instruction to each power supply control device of the other information processing devices upon each activation;

notifying each power supply control device of the other information processing devices of a next power-up date and time, having each power supply control device enter the next power-up date and time, and issuing a power-down instruction to each of the other information processing device each time a power-down date and time comes; and

each power supply control device of said other information processing devices performing a power-up process upon receipt of the power-up instruction or when the entered power-up date and time comes.

3. The power supply control method according to

claim 1, wherein

said power-up date and time given to each of said power supply control devices of said other information processing devices is obtained by any of said information processing devices or each of said other information processing devices adding an arbitrary margin to a power-up date and time in said predetermined power-up/down schedule.

4. The power supply control method according to claim 2, wherein

said power-up date and time given to each of said power supply control devices of said other information processing devices is obtained by any of said information processing devices or each of said other information processing devices adding an arbitrary margin to a power-up date and time in said predetermined power-up/down schedule.

5. The power supply control method according to claim 1, wherein

said arbitrary information processing device does not give the power-down instruction and the next power-up date and time before a power-down permission condition entered in advance of a

current and other information processing devices is satisfied although the power-down date and time comes.

6. The power supply control method according to claim 2, wherein
said arbitrary information processing device does not give the power-down instruction and the next power-up date and time before a power-down permission condition entered in advance of a current and other information processing devices is satisfied although the power-down date and time comes.

7. The power supply control method according to claim 1 , wherein
said power-up instruction or power-down instruction is sequentially issued at predetermined startup intervals or power-down intervals.

8. The power supply control method according to claim 2, wherein
said power-up instruction or power-down instruction is sequentially issued at predetermined startup intervals or power-down intervals.

9. An information processing apparatus which is an arbitrary information processing device in a plurality of information processing devices in a computer system in which a power supply control device is provided for each of the plurality of information processing devices connected to a network, comprising:

a power-up/down schedule storage unit storing predetermined power-up/down schedules of said arbitrary information processing device and other information processing devices;

a power-up instruction unit instructing each power supply control device of other information processing devices to perform a power-up process at each activation process; and

a power-down instruction unit instructing each power supply control device to perform a power-down process and notifying each power supply control device of a next power-up date and time each time power-down date and time comes according to said predetermined power-up/down schedule.

10. The information processing device according to claim 9, wherein

said next power-up date and time given to each power supply control device is obtained by any of said information processing devices or each of said information processing devices adding an arbitrary margin to a power-up date and time in a power-up/down schedule stored in said power-up/down schedule storage unit.

11. The information processing device according to claim 9 , further comprising:

a power-down permission condition storage unit for storing a power-down permission condition of a predetermined current and other information processing devices; and

does not give the power-down instruction and the next power-up date and time before a power-down permission condition is satisfied although the power-down date and time comes.

12. The information processing device according to claim 9 , wherein

said power-up instruction or power-down instruction is sequentially issued at predetermined startup intervals or power-down intervals.

13. A power supply control device in a computer system in which a power supply control device is provided for each of a plurality of information processing devices connected to a network, comprising:

a power-down unit storing a next power-up date and time when the next power-up date and time is received together with a power-down instruction, and performing a power-down process on an information processing device of a current system; and

a power-up unit performing a power-up process on the current information processing device when a power-up instruction is received or said stored power-up date and time comes.

14. A computer-readable storage medium storing a program used to direct a computer to realize the functions of:

instructing each power supply control device of other information processing devices to perform a power-up process at each activation process; and

instructing each power supply control device to perform a power-down process and notifying each power supply control device of a next power-up date

and time each time power-down date and time comes according to a predetermined power-up/down schedule.

15. A computer data signal embodied in a carrier wave storing a computer program used to direct a computer to realize the functions of:

instructing each power supply control device of other information processing devices to perform a power-up process at each activation process; and

instructing each power supply control device to perform a power-down process and notifying each power supply control device of a next power-up date and time each time power-down date and time comes according to a predetermined power-up/down schedule.